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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

K. ISHIBASHI et al.

Serial No. 10/731,152

Group Art Unit: 2815

Filed: December 10, 2003

Examiner: J. FENTY

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

RESPONSE

Commissioner for Patents
PO Box 1450
Alexandria VA 22313-1450

Sir:

In response to the Office Action of December 17, 2004

Applicants respond as follows.

Remarks begin on page 2 of this paper.